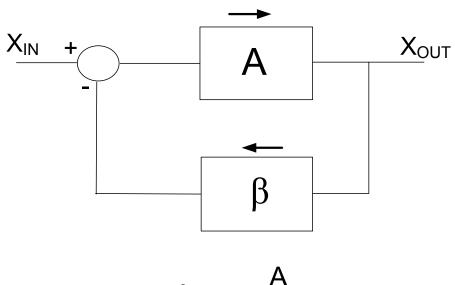
# EE 230 Lecture 10

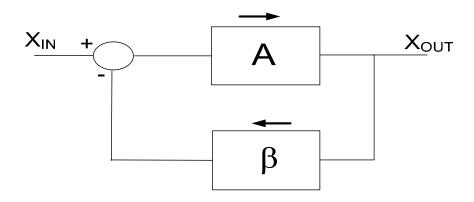
Operational Amplifiers and Basic Applications



$$A_{FB} = \frac{A}{1 + A\beta}$$

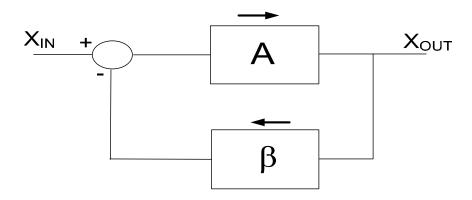
for A 
$$\beta >> 1$$

$$A_{\mathsf{FB}} \cong \frac{1}{\beta}$$



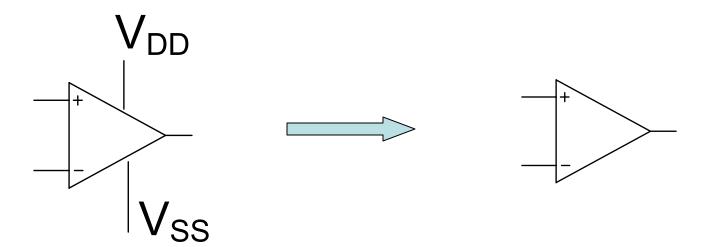
Feedback significantly and simultaneously improves most parameters of interest by a factor of about  $1+A\beta$ 

Feedback properties is the major driving factor behind the evolution and use of operational amplifiers

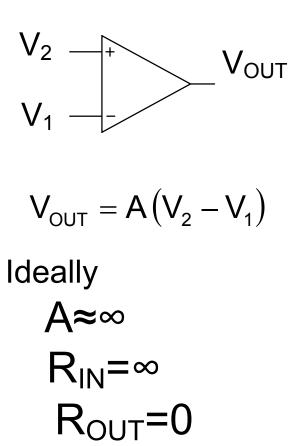


Feedback significantly and simultaneously improves most parameters of interest by a factor of about 1+A $\beta$ 

## For a VCVS Op Amp



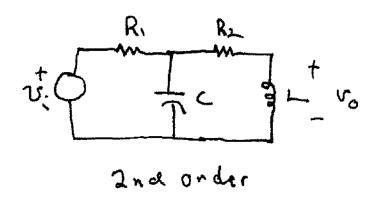
## For a VCVS Op Amp

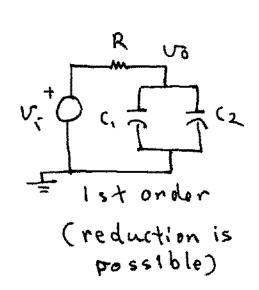


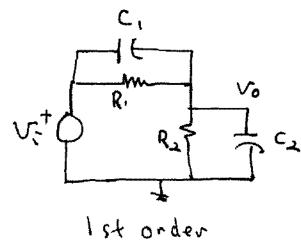
Theorem: If a network has no frequencydependent amplifiers, then the order of the transfer function of the network is less than or equal to the number of energy storage elements.

(It is usually equal to: except when reduction or pole-zero cancellation occurs)

Examples

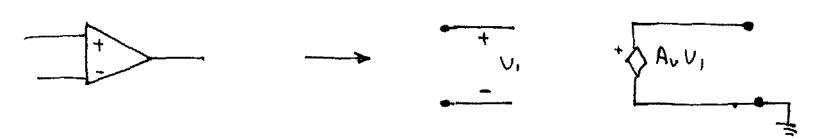






(a type of reduction is possible - details beyond scope at this time)

## Model for Op Amp



The "E" source in SPICE can be used to model the op amp

Ideal Op Amp: Simply make Av large  $(A^2 \otimes)$  (may be  $A_v = 10^8$ )

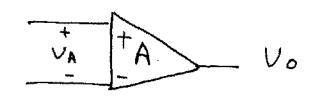
Practical de op amp model:

Av = data sheet de gain

I deal model is adequate in many applications (5)

Multiple models of the opamp will be introduced that emphasize specific characteristics or support specific applications

Invariably, if a nonideal op amp model is necessary to adequately predict the performance of a feedback circuit, the feedback circuit is not very practical.



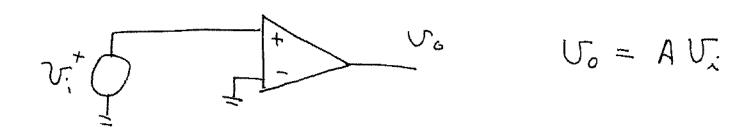
If Vo is some finite value

"Virtual short" exists between "+" and "-"
terminals of op amp

Input port has properties of open circuit (Rin = 0) and short circuit (VA = 0) simultaneously

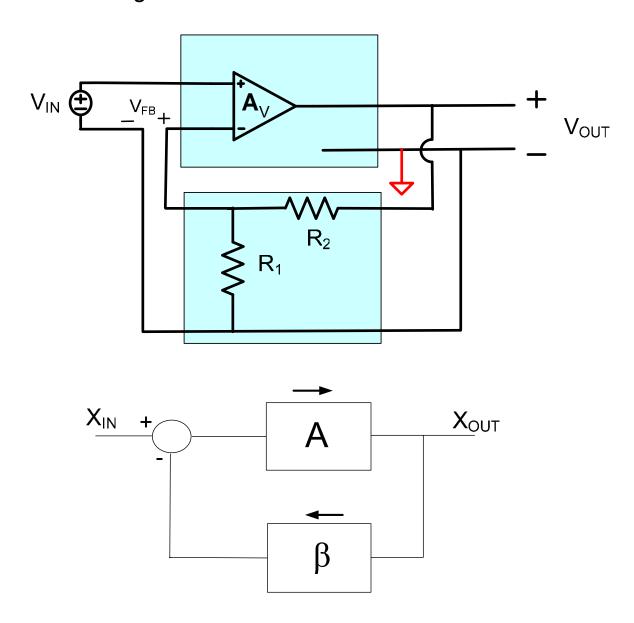
Input port of op amp termed "Null Port"

Op Amp is almost never used without feedback as an amplifier!

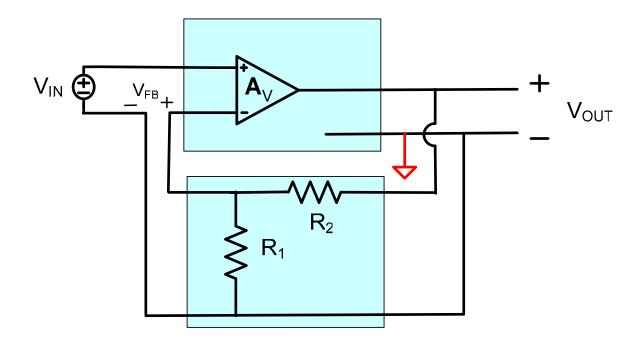


Reasons: A highly variable
"A" highly nonlinear
Offset (discussed later) will
drive output to saturation

## Consider the following circuit



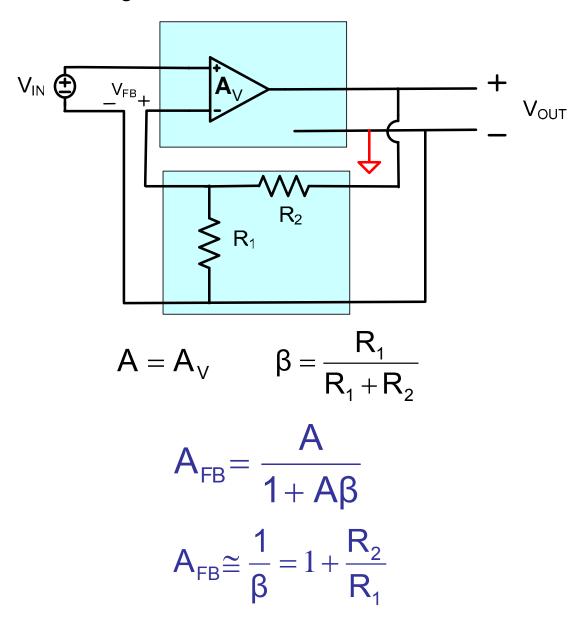
#### Consider the following circuit



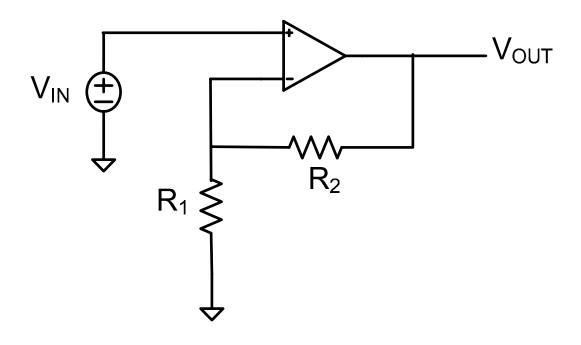
This is exactly the feedback architecture in the case where the input and output variables are voltages

This is a voltage feedback amplifier

#### Consider the following circuit

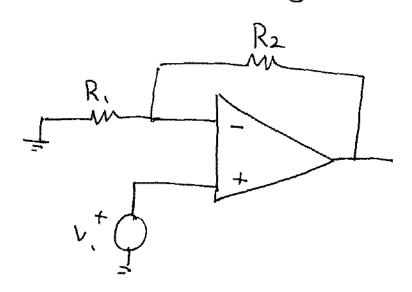


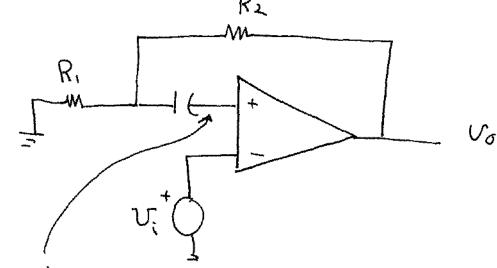
## **Basic Noninverting Amplifier**



$$A_{FB} = \frac{V_{OUT}}{V_{IN}} \cong 1 + \frac{R_2}{R_1}$$

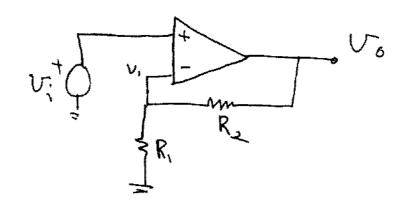
Even though the input current to the op amp is nearly O, a dc path to each of the op amp input terminals must always be provided.





No de path!

Direct Analysis



$$V_{i} = \frac{R_{i}}{R_{i} + R_{2}} V_{0}$$

$$V_{i} = V_{i}$$

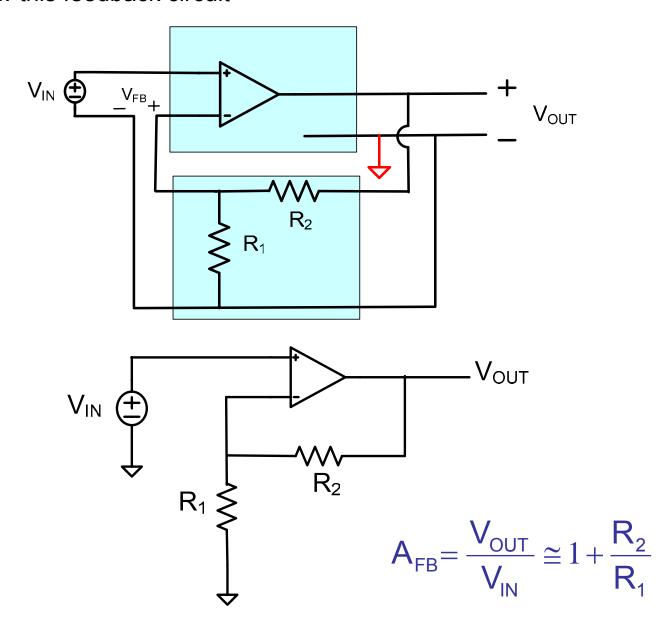
$$V_{i} = V_{i}$$

$$V_{i} = V_{i}$$

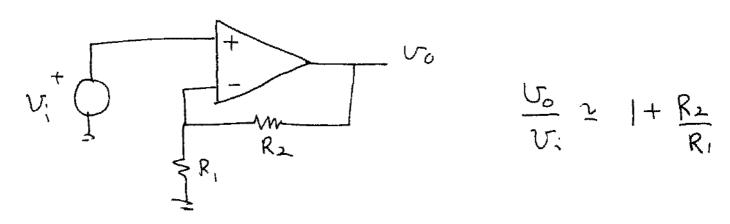
$$\frac{V_o}{V_i} = 1 + \frac{R_2}{R_i}$$

- Direct Analysis is much easier than feedback analysis
- Feedback analysis seldom used when analyzing feedback amplifiers
- Many feedback amplifiers do not satisfy plant of architectures but rather modified feedback architectures

#### Redraw this feedback circuit



- All feedback structures do derive their useful properties from the feedback concept

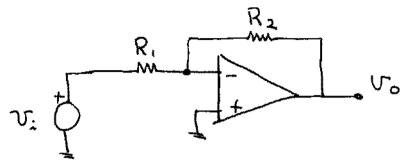


If  $\frac{R_2}{R_i} = 9$ , how much error was introduced by assuming  $A = \infty$ ? Assume  $A_i = 10^5$ 

$$\frac{U_0}{V_0} = \frac{A_0}{1+\beta A_0} = \frac{10^5}{1+\left(\frac{1}{10}\right)10^5} = \frac{10^5}{10,001} = 9.9990$$

error 1 part in 10,000 = .01%

Basic Inverting Amplifier

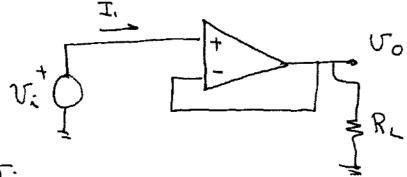


$$\frac{V_{:}}{R_{i}} + \frac{V_{o}}{R_{2}} = 0$$

$$\Rightarrow \frac{V_{o}}{V_{i}} = -\frac{R_{2}}{R_{i}}$$

This circuit does not satisfy directly the "A-B" feedback Structure as a voltage in / voltage out amplifier

(It can be shown that this is a transresistance feedback amplifier but details will not be provided at this time)



- · One of the most widely used op amp circuits
- · Provides a signal to a load that is not affected by source impedance
- · Buffer is a special case of basic noninventing amplifier

$$\frac{V_0}{R_1} = \frac{V_0}{V_1} = 1 + \frac{Q_0}{R_1}$$

$$R_1 = 0$$

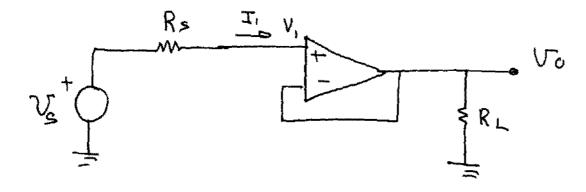
$$R_2 = 0$$

$$R_3 = 0$$

$$R_4 = 0$$

$$R_5 = 0$$

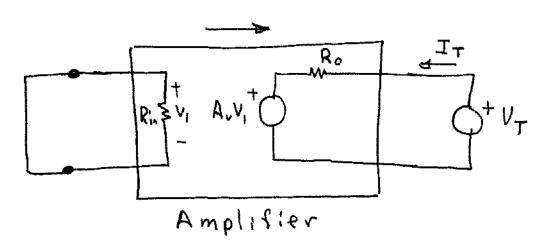
## Effects of Rs



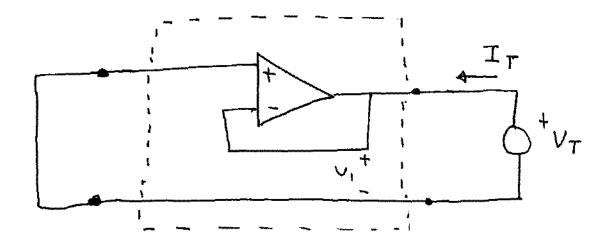
Since  $I_1 = 0$ ,  $V_1 = V_5$  $V_0 = V_1 = V_5$  independent of  $R_5$ 

Note: Buffer widely used to eliminate (reduce) loading effect of a following stage.

Rout Derivation.



$$Rout = \frac{V_T}{I_T}$$

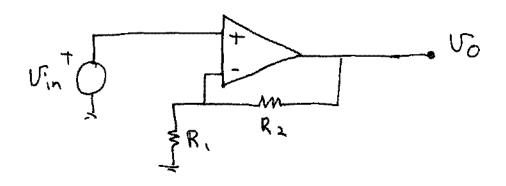


If Op Amp is ideal

$$V_1 = 0 \Rightarrow I_T = \infty$$

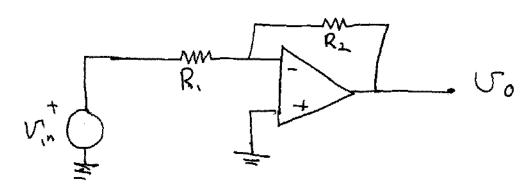
$$\therefore R_{\text{out}} = \frac{V_T}{I_T} = \frac{V_T}{\infty} = 0$$

# Basic Noninverting Amplifier

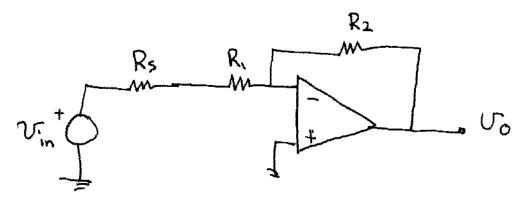


$$\frac{V_0}{V_{in}} = 1 + \frac{R_2}{R_1}$$

## Basic Inverting Amplifier

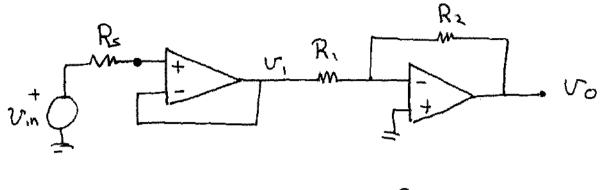


$$A_{v} = -\frac{R_{t}}{R_{i}}$$



$$A_{V} = -\frac{R_{2}}{R_{1} + R_{5}}$$

## Consider



$$V_{0} = V_{1}$$

$$V_{0} = -\frac{R_{2}}{R_{1}} V_{1}$$

$$V_{0} = -\frac{R_{2}}{R_{1}} V_{2}$$

$$V_o = -\frac{R_2}{R_i} V_{i}$$

- Buffer has improved input impedance of inverting amplifier
- a Widely used if input impedance is a problem